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*Specializing in Patent, Trademark  
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Steven M. Rabin  
Robert H. Berdo, Jr.  
Phillip G. Avruch  
Allen Wood  
Qixia Zhang, Pat. Agent (China)

Suite 500, 1101 14<sup>th</sup> Street, N.W.  
Washington, D.C. 20005  
Tel.: (202) 371-8976 Fax: (202) 408-0924  
E-Mail: firm@rabinchamp.com

*In Affiliation with*  
Weiss, Moy & Harris, PC  
having offices in Arizona and Nevada,  
and in Washington, DC with  
Rabin & Berdo, PC

June 27, 2003

PATENT TRADEMARK OFFICE



23995

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**Box: PATENT APPLICATION**

Commissioner for Patents  
Washington, D.C. 20231

Attorney Reference: MAE 288

**Re:** New Patent Application of: Hiromitsu MIYAMOTO  
**Title:** SYNCHRONIZATION ERROR DETECTION CIRCUIT

Sir:

Please find attached hereto an application for patent that includes:

- ☒ Specification, Claims and Abstract (32 Pages)
- ☒ 8 Sheets of Formal Drawings (Fig. 1 through Fig. 8)
- ☐ Inventor's Declaration and Power of Attorney ( Pages)
- ☒ Claim for priority and one Priority document
- ☐ Assignment document with cover page ( Pages)
- ☐ Fee (see formula below)

The right of priority is claimed under 35 USC §119 of Japanese Application No. 2002-192492, filed July 1, 2002.

Respectfully submitted,

June 27, 2003

Date

SMR/tl

Steven M. Rabin (Reg. No. 29,102)  
(Customer No. 23995)

